




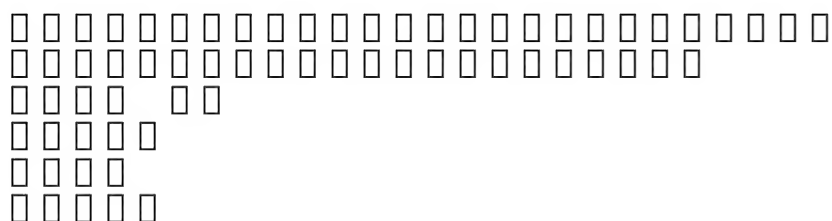
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Diagram illustrating a memory layout or data structure. The layout consists of a grid of 4 rows and 32 columns. The grid is divided into four horizontal sections. The top section contains 16 small squares, followed by a gap of 16 squares. The second section contains 16 small squares, followed by a gap of 16 squares. The third section contains 16 small squares, followed by a gap of 16 squares. The bottom section contains 16 small squares, followed by a gap of 16 squares. The gaps are labeled with '00' and '00' respectively.





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